Course Content

Transmission

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Name of the course:	Synchronous Digital Hierarchy
Duration:	5 days
Venue:	Welisara

Course content:

- PDH debriefing
- Limitation of PDH
- Benefits of SDH
- Network configurations and Network Elements
- SONET and SDH possible bit rates
- SDH mapping and multiplexing structure
- Functions of Container, Virtual Container, TU and AU
- Sizes and nominal speeds of each and every block
- 2M, 34M and 140M mapping in to STM1
- ATM mapping
- Functionality of Path Over Heads (POH)
- BIP as error detection techniques
- Pointer Generation and Interpretation
- Functionality of Section Over Heads (SOH)
- Network Synchronization
- Equipment configurations
- SDH anomalies, Defects, Failures and Alarms
- Protection Architecture
- 2M numbering and Cross Connection
- Operation and Maintenance
- Hands on training SDH MUX equipments